



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Ohtani

Art Unit : 1765

Serial No. : 09/596,755

Examiner : Robert Kunemund

Filed : June 15, 2000

Title : MANUFACTURING METHOD OF SEMICONDUCTOR AND
MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

TRANSMITTAL LETTER AND AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	44	-	47	=	0	\$0
Independent	7	-	7	=	0	\$0
First Presentation of Multiple Dependent Claims						\$0

Applicant hereby petitions under 37 C.F.R. §1.136 for a 1 month extension of time.

\$110

TOTAL FEE DUE

\$110

A check for \$110 is attached.

Respectfully submitted,

Date:

October 23, 2003

William G. Hughes, Jr.

Reg. No. 46,112

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